

MULTIPLEXER CIRCUIT FOR CONVERTING PARALLEL DATA
INTO SERIAL DATA AT HIGH SPEED AND SYNCHRONIZING
THE SERIAL DATA WITH A CLOCK SIGNAL

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ABSTRACT OF THE DISCLOSURE

10 A multiplexer circuit converts parallel data into
serial data synchronized with an internal clock signal,
and the multiplexer circuit has a logic circuit, a load
circuit, and a plurality of switching elements. The
logic circuit processes the internal clock signal and the
15 parallel data. The load circuit and the plurality of
switching elements are connected in series between a
first power source line and a second power source line.
Each of the switching elements is controlled in
accordance with an output of the logic circuit.